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APPLICANTS Naoshi Adachi, Tokyo, JAPAN; Masahiko Nakamae, Tokyo, JAPAN; ** CONTINUING DATA ***** <i>SD</i> This application is a 371 of PCT/JP03/13514 10/22/2003 ** FOREIGN APPLICATIONS ***** <i>SD</i> JAPAN 2002-307478 10/22/2002				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>Stonetta Chan</i> <i>SD</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 11
			INDEPENDENT CLAIMS 2	
ADDRESS Kubovcik & Kubovcik The Farragut Building Suite 710 900 17th Street NW Washington, DC20006				
TITLE Pasted soi substrate, process for producing the same and semiconductor device				
FILING FEE RECEIVED 920	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	